DAQ sNews

- Have four new server class rack mount computers DAQ 1+1 and DQM 1+1
  - Installation expected approximately tomorrow, or Thursday
  - May need mechanical help for installation
  - Digital I/O break-out cables have been ordered

- New proxy http://lariat-dqm.fnal.gov
  - Allow connection only to port 5000 on lariat-daq01
  - Visible off-site for possible remote shifts

- Found pedestal shift on TPC wires more global
  - Don't blame the CAEN
  - See Dung's talk

More DAQ sNews

- Proposing a BUSY mechanism to hold off triggers when buffers nearly full
  - Addressing the event counter mismatch errors
  - Details following

- Johnny found possible dead-time in V1740 readout
  - Have non-zero minimum in trigger spacing (see Johnny)

Gettin' BUSY, a decent proposal

- See regular problems of event counter mismatch
  - Some V1740s, V1751s miss trigger
  - Also observed in similar DS-50, with V1720s and V1495 for trigger
  - Suspect buffers are full or nearly so, and need trigger suppression
  - Implement an "Almost Busy" signal when <= 2 (out of 64) buffers are full, allowing for BUSY propagation latency
  - Propose V1740s' BUSY to *veto* the *delayed trigger*
  - V1751s' BUSY will be required *off* on *fast trigger*
  - What about spare V1740, not necessarily in synch with other V1740s?   Ignore?

BUSY paradigm

2 V1751s

Logical OR ➜

V1495

Increasing time

Require <OFF> in fast trigger paths
No one sees triggers

8 V1740s

Logical OR ➜

Require <VETO> in one delayed veto path
V1740 see no trigger
V1751 still saw earlier fast trigger